



Applicant: Dempsey et al.
Docket No: AD-332J
Serial No: 10/627,500
Title: Integrated Digital Calibration Circuit and
Digital to Analog Converter (DAC)
Atty: Jason D. Shanske, Reg. No. 43,915

1 of 6

1/6

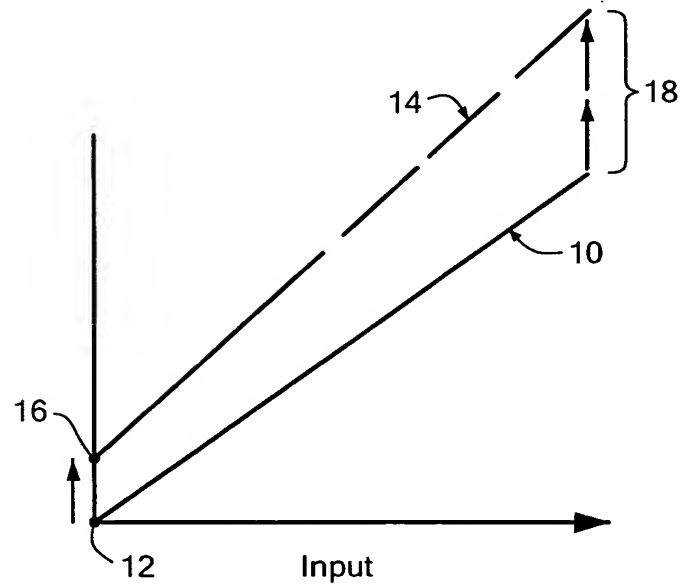


FIG. 1

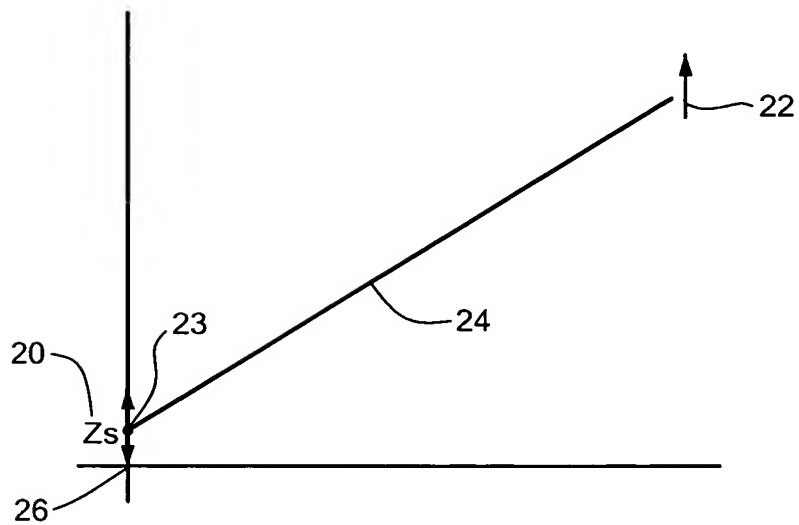


FIG. 2



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2 of 6

2/6

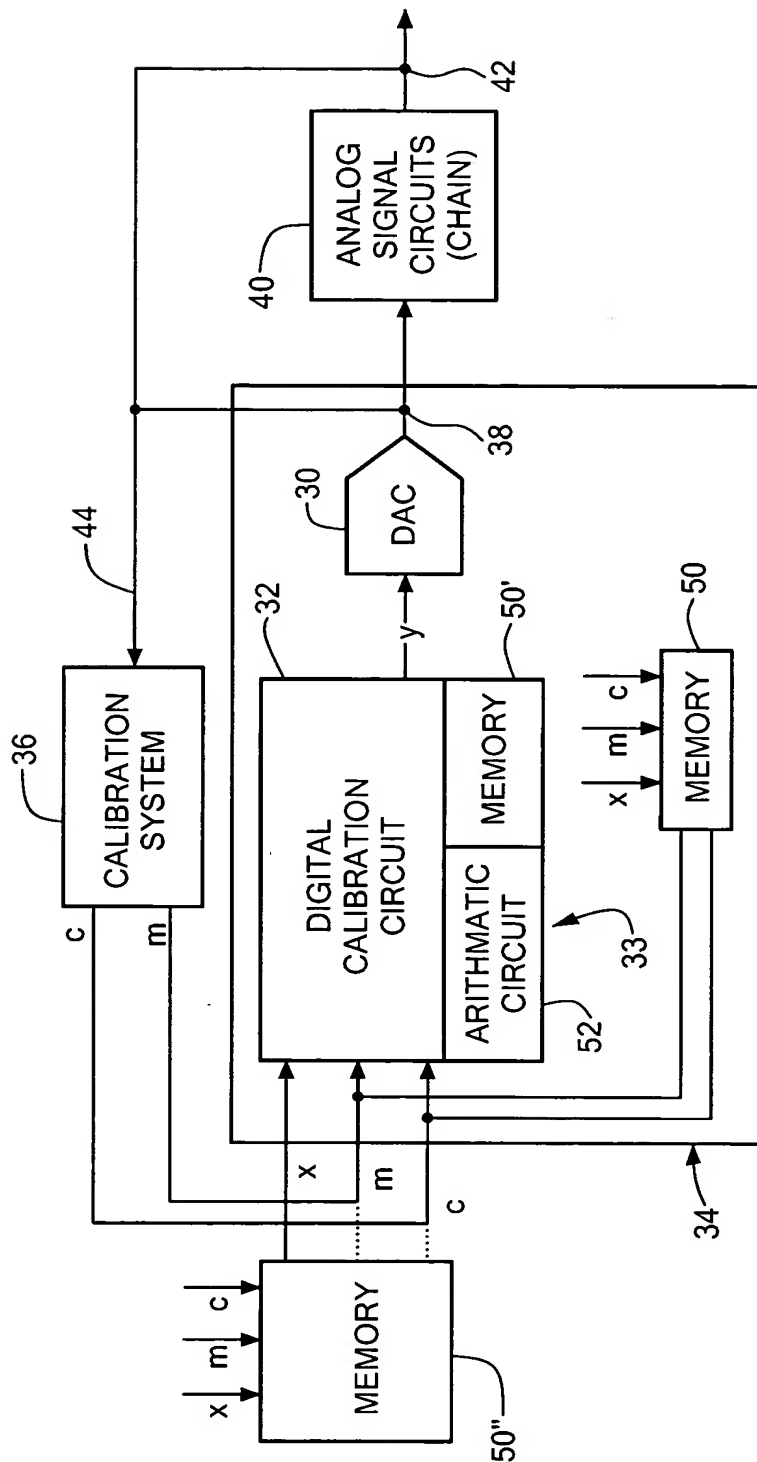


FIG. 3



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3 of 6

3/6

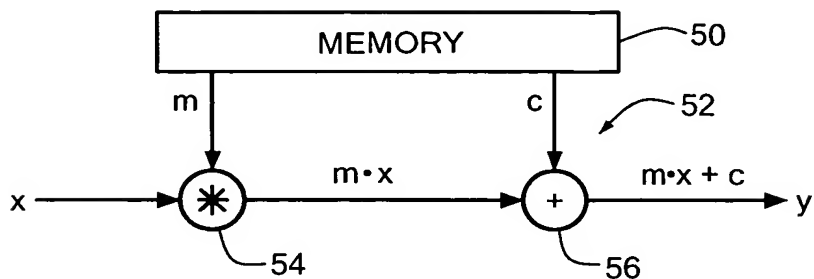


FIG. 4A

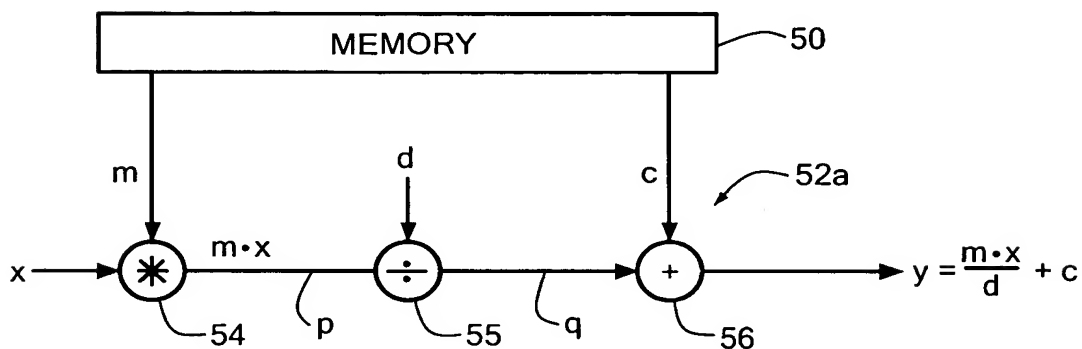


FIG. 4B

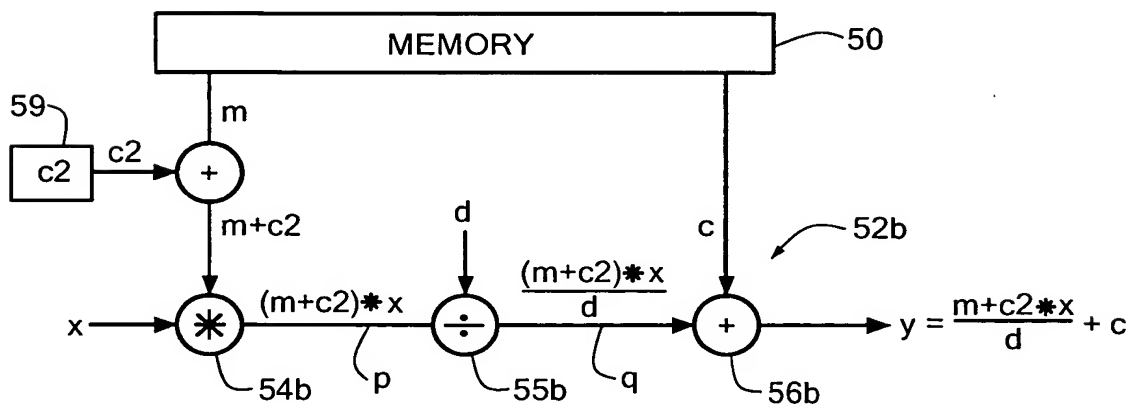
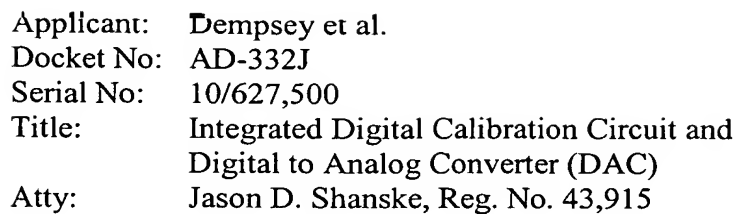


FIG. 4C





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5 of 6

5/6

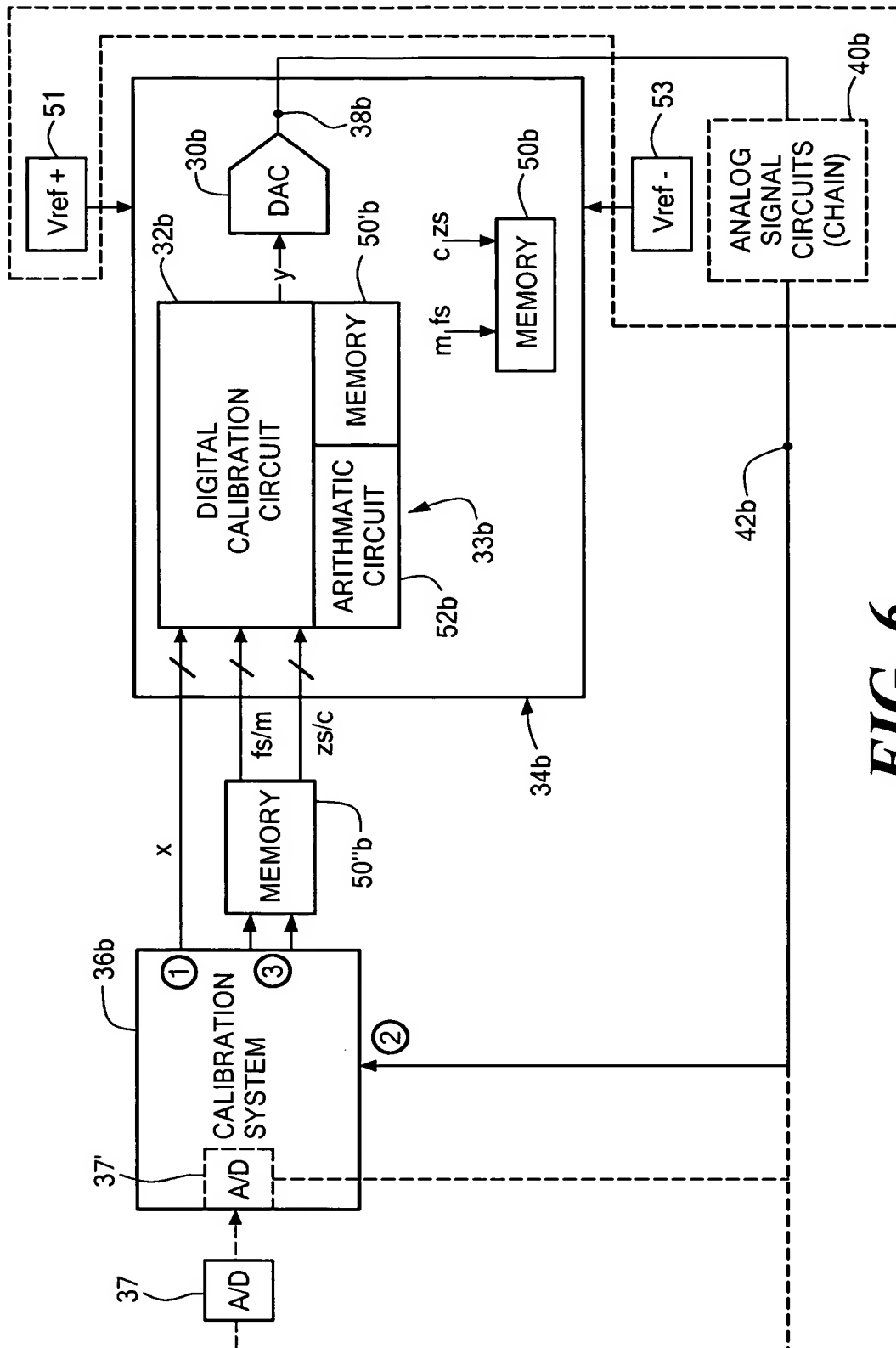


FIG. 6



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6 of 6

6/6

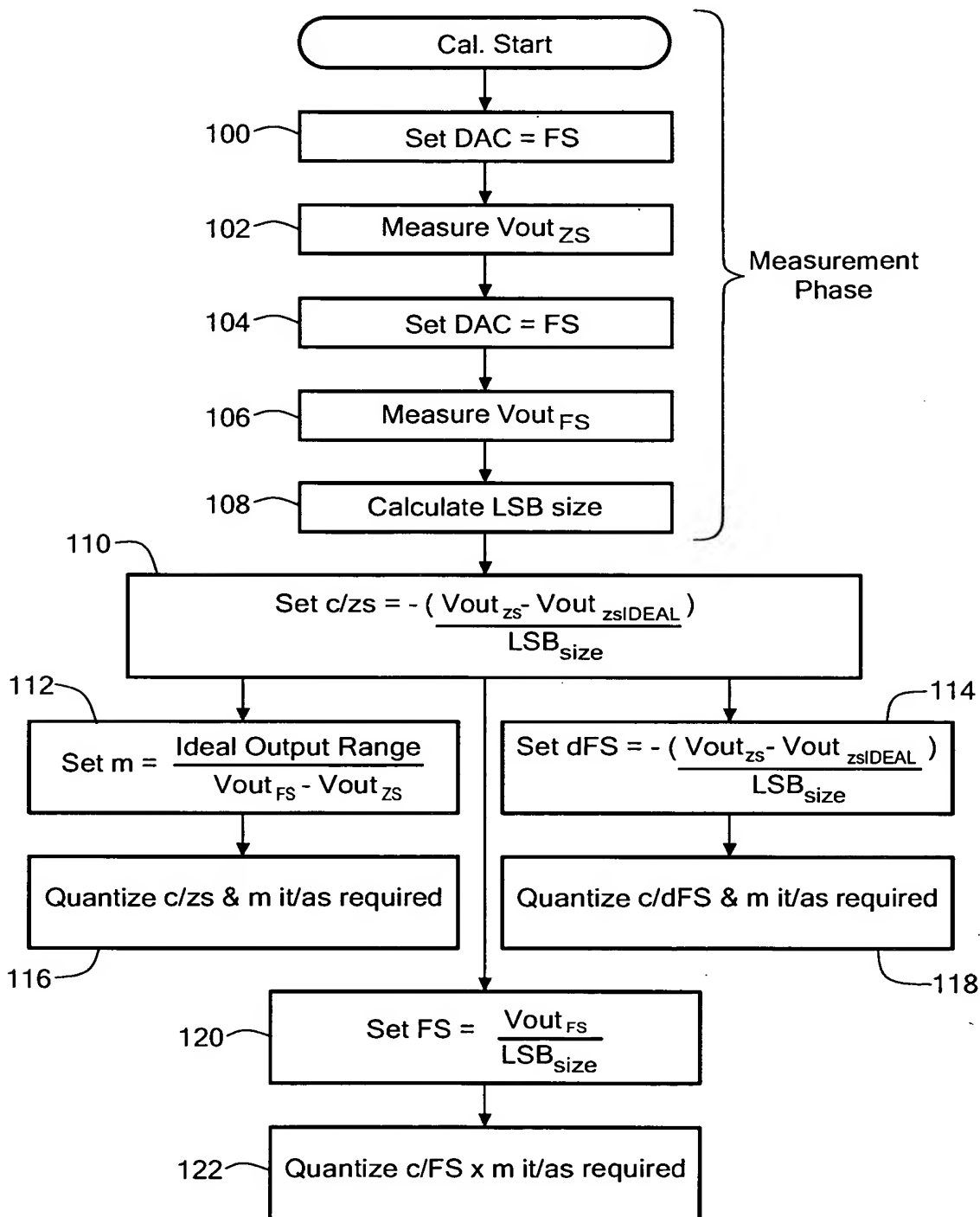


FIG 7